

**IN THE CLAIMS:**

Please amend the claims as follows:

1. (Original) A static electricity countermeasure component comprising:  
a varistor layer; and  
a board laminated with the varistor layer;  
wherein the varistor layer comprises a material including at least bismuth oxide, the bismuth oxide is diffused to the board by sintering the varistor layer and the board, and a bismuth oxide diffusing layer is provided at the board.
2. (Original) The static electricity countermeasure component of claim 1, wherein the board is an alumina board.
3. (Original) The static electricity countermeasure component of claim 2, wherein the board is formed by laminating a glass ceramic layer including glass on the alumina board.
4. (Original) The static electricity countermeasure component of claim 3, wherein the glass is diffused in the alumina board, and a glass diffusing layer is provided at the alumina board.
5. (Original) The static electricity countermeasure component of claim 3, wherein an adhesive layer is provided between the glass ceramic layer and the alumina board, the glass is diffused in the alumina board by way of the adhesive layer, and a glass diffusing layer is provided at the alumina board.
6. (Original) The static electricity countermeasure component of claim 1, wherein a glass ceramic layer including glass is laminated on the varistor layer.
7. (Original) The static electricity countermeasure component of claim 1, wherein the varistor layer is formed by laminating and sintering a plurality of unsintered green sheets

including a powder of a varistor material, and a mean particle diameter of the powder of the varistor material falls in a range of 0.5-2.0  $\mu\text{m}$ .

8. (Original) The static electricity countermeasure component of claim 7, wherein the varistor material comprises zinc oxide as a major component and at least bismuth oxide as an additive, and a mean particle diameter of a powder of the bismuth oxide is equal to or smaller than 1.0  $\mu\text{m}$ .

9. (Original) The static electricity countermeasure component of claim 1, wherein an adhesive layer is provided between the varistor layer and the board, and the bismuth oxide is made to be diffused in the board by way of the adhesive layer.

10. (Original) The static electricity countermeasure component of claim 1, wherein the board is constituted by a circuit board having an electronic component circuit formed therein.

11. (Original) The static electricity countermeasure component of claim 1, wherein the board is laminated with a circuit layer on which an electronic component circuit is formed, on a side opposed to a side on which the varistor layer is laminated.

12. (Original) The static electricity countermeasure component of claim 1, wherein the board is constituted by a low temperature sintering ceramic board.

13. (New) The static electricity countermeasure component of claim 1, wherein the bismuth oxide diffusing layer is provided between the varistor layer and the board.

14. (New) The static electricity countermeasure component of claim 13, wherein the bismuth oxide diffusing layer is made of bismuth oxide and the material of the board.

15. (New) The static electricity countermeasure component of claim 1, wherein the bismuth oxide diffusing layer is made of bismuth oxide and the material of the board.